



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application)	Art Unit: 1765
Applicants:	Satoshi Komiya et al.)	Confirmation No.: 2326
Serial No.:	10/049,875)	I hereby certify that this correspondence is being deposited with the United
Filed:	February 12, 2002))	Postal Service as first class mail in an envelope addressed to: MAIL STOP
For:	SILICON WAFER AND METHOD FOR MANUFACTURE THEREOF, AND METHOD OF)	FEE AMENDMENT / Commissioner for Patents / P.O. Box 1450 / Arlington, Virginia 22313-1450
	EVALUATION OF SILICON WAFER)))	Gerald T. Shekleton Reg. No. 27,466
Examiner:	Robert M. Kunemund)	

AMENDMENT

MAIL STOP FEE AMENDMENT Commissioner for Patents P.O. Box 1450 Arlington, Virginia 22313-1450

Sir:

The Office Action of June 3, 2003 has been carefully reviewed and the following amendment and remarks are made in response thereto.